

P-L496G-CELL01

STM32 discovery pack for 2G/3G cellular to cloud

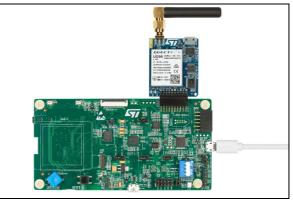
Data brief

Features

- STM32L496AGI6 microcontroller featuring 1 Mbyte of Flash memory and 320 Kbytes of RAM in a UFBGA169 package
- USB OTG HS
- On-board current measurement
- SAI Audio CODEC
- ST-MEMS digital microphones
- 8-Mbit PSRAM
- 2 user LEDs
- 1 user and 1 reset push-buttons
- 4-direction joystick with selection button
- Board connectors:
 - Camera 8 bit
 - USB with Micro-AB
 - Stereo headset jack including analog microphone input
 - microSD[™] card
- Board expansion connectors:
 - Arduino™ Uno V3
 - STMod+
- Board expansion features:
 - Quectel UG96 worldwide cellular modem penta-band 2G/3G module, 7.2 Mbps downlink, 5.76 Mbps uplink
 - Modem reset red LED and modem signaling green LED
 - Switchable SIM interface, eSIM and MicroSIM
 - Pulse 2G/3G SMA antenna for frequency ranges: 850 / 900 / 1800 / 1900 / 2100 MHz
- Flexible power-supply options: ST-LINK, USB V_{BUS}, or external sources
- On-board ST-LINK/V2-1 SWD,TAG debugger/programmer with USB reenumeration capability: mass storage, virtual COM port and debug port

February 2018

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Picture is not contractual.

- Comprehensive free software libraries and examples available with the STM32Cube package
- Support of a wide choice of Integrated Development Environments (IDEs) including IAR™, Keil[®], GCC-based IDEs

Description

The P-L496G-CELL01 STM32 discovery pack for 2G/3G cellular to cloud (STM32-C2C/2G-3G) is a turnkey development platform for cellular and cloud technology based solutions.

The pack is composed of an STM32L496AGI6based low-power discovery mother board with preloaded firmware, and an STMod+ cellular expansion board with antenna.

General information

The firmware of the P-L496G-CELL01 discovery pack runs on the STM32L496AGI6 $\mbox{Arm}^{\mbox{\ensuremath{\mathbb{R}}}}\mbox{-based device}.$

arm

System requirements

- Windows[®] OS (7, 8 and 10), Linux[®] 64-bit or macOS[®]
- USB Type-A to Micro-B cable

Development toolchains

- Keil[®] MDK-ARM^(a)
- IAR[™] EWARM^(a)
- GCC-based IDEs including free SW4STM32 from AC6

Demonstration software

The demonstration software, included in the STM32Cube MCU Package, is preloaded in the STM32 Flash memory for easy demonstration of the device peripherals in standalone mode. The latest versions of the demonstration source code and associated documentation can be downloaded from the *www.st.com* webpage.

Ordering information

To order the P-L496G-CELL01 discovery pack refer to Table 1.

Table 1. Ordering information

Order code	Target STM32
P-L496G-CELL01	STM32L496AGI6

a. On Windows[®] only

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Technology partners

EMNIFY:

IoT connectivity platform eSIM

QUECTEL:

• Penta-band 2G/3G module

EXOSITE:

• Cloud data management

GROVESTREAMS:

• IoT platform

Revision history

Date	Revision	Changes
12-Feb-2018	1	Initial version
22-Feb-2018	2	Updated <i>Features</i> to remove reference to Arm [®] Mbed™

Table 2. Document revision history



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